A Browser-based RISC-V Simulator

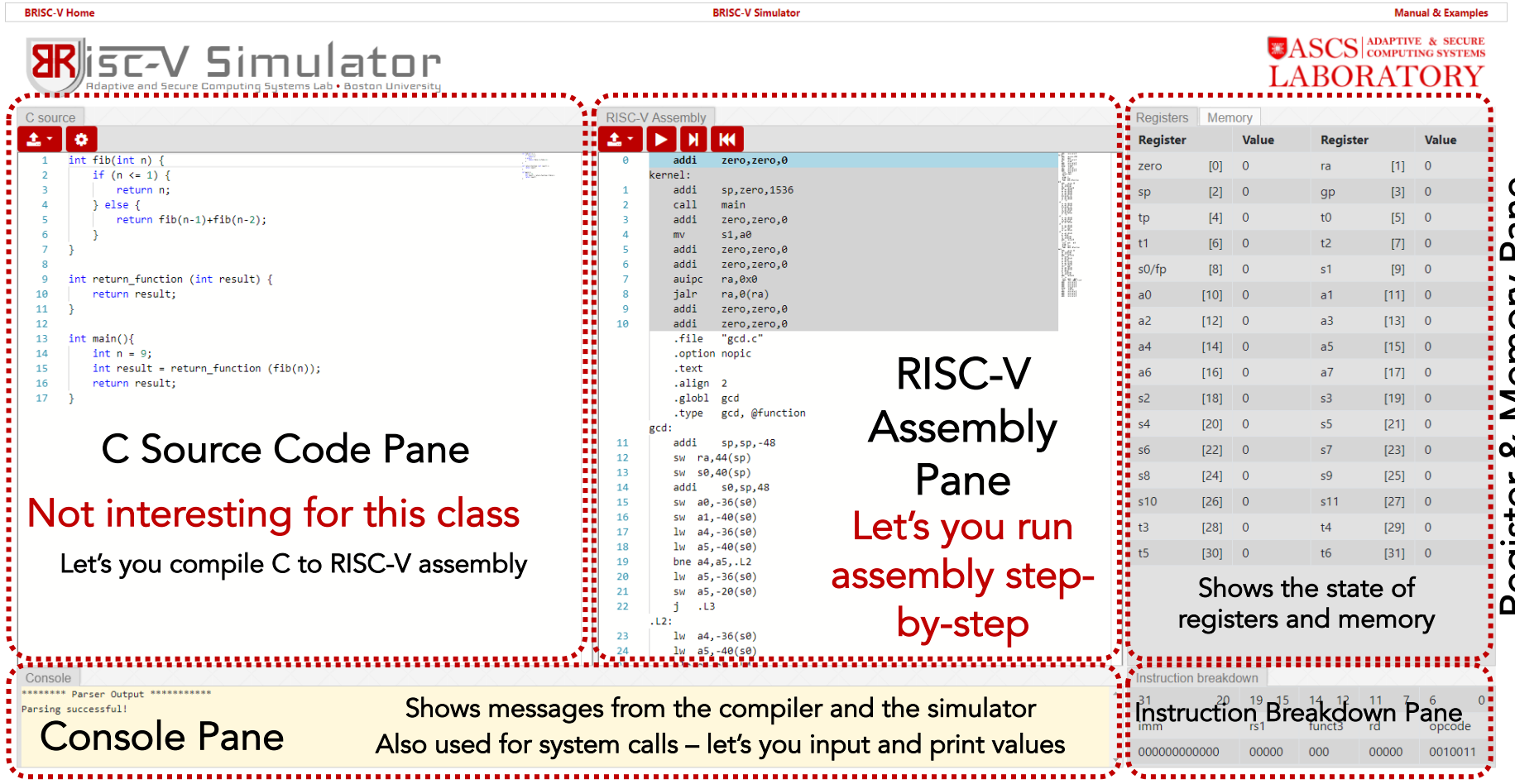
Adaptive and Secure Computing Systems (ASCS) Laboratory

BRISC-V Simulator

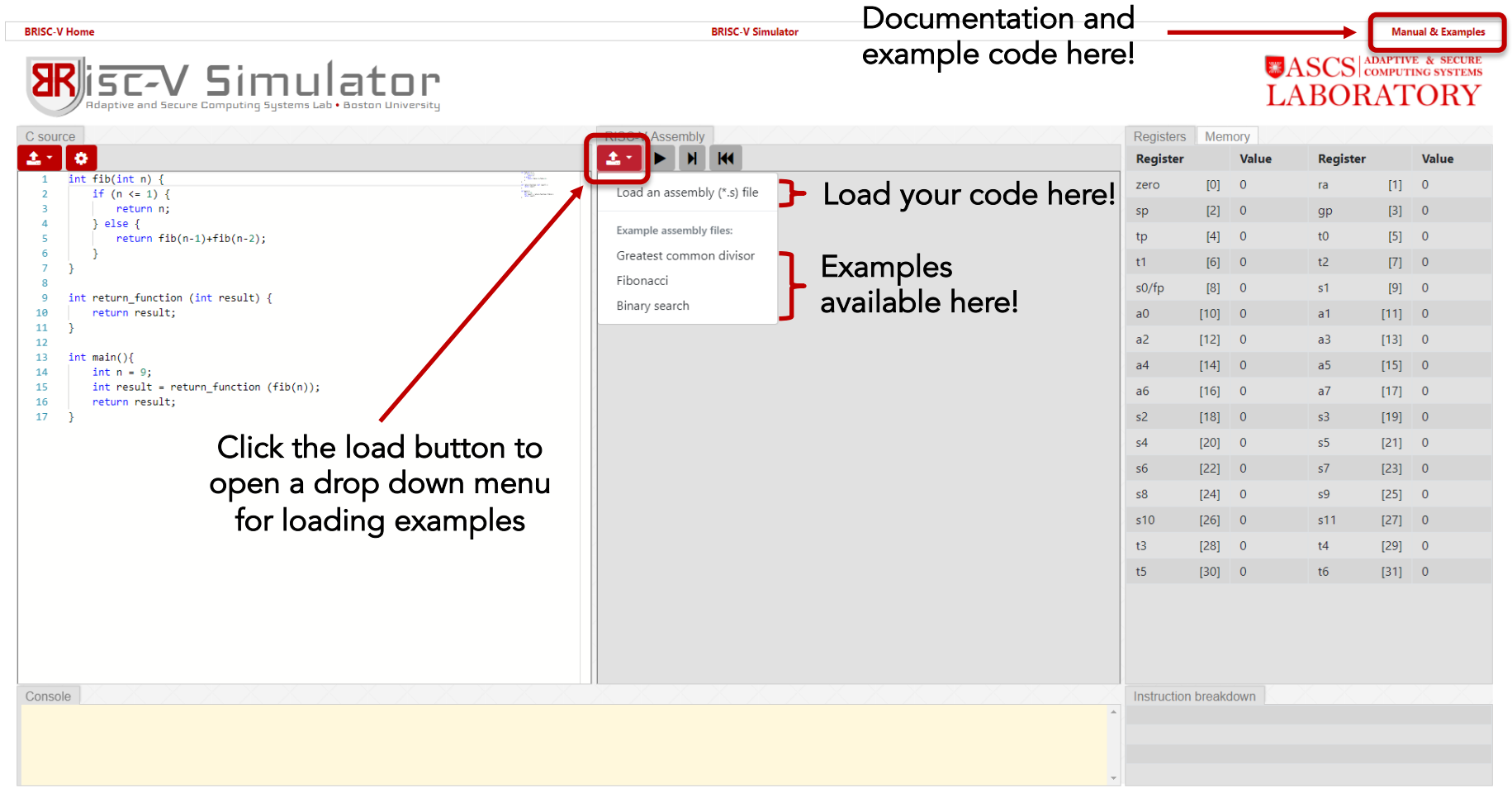
- BRISC-V Simulator let's you:
  - Run RISC-V assembly code in the browser
  - Debug hand-written assembly
  - Run code until completion or until it hits a breakpoint
  - Step through execution, instruction-by-instruction
  - View the state of memory and registers at each step
  - View how each instruction is constructed – opcodes, registers, immediate values etc.

https://ascslab.org/research/briscv/simulator/simulator.html

Let's get Familiar with the GUI



Don't have valid RISC-V assembly code to start with?



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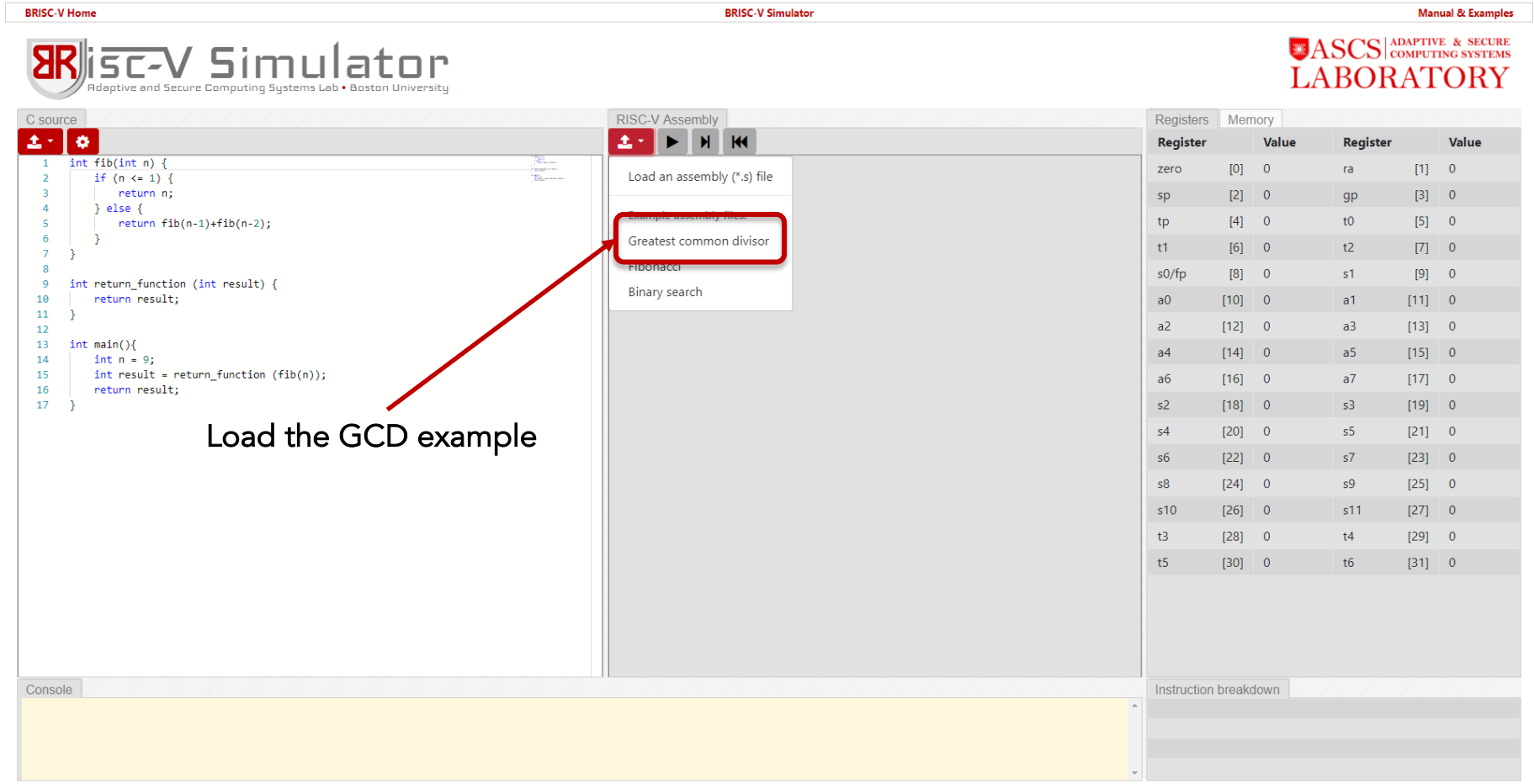

Kernel and User Instructions

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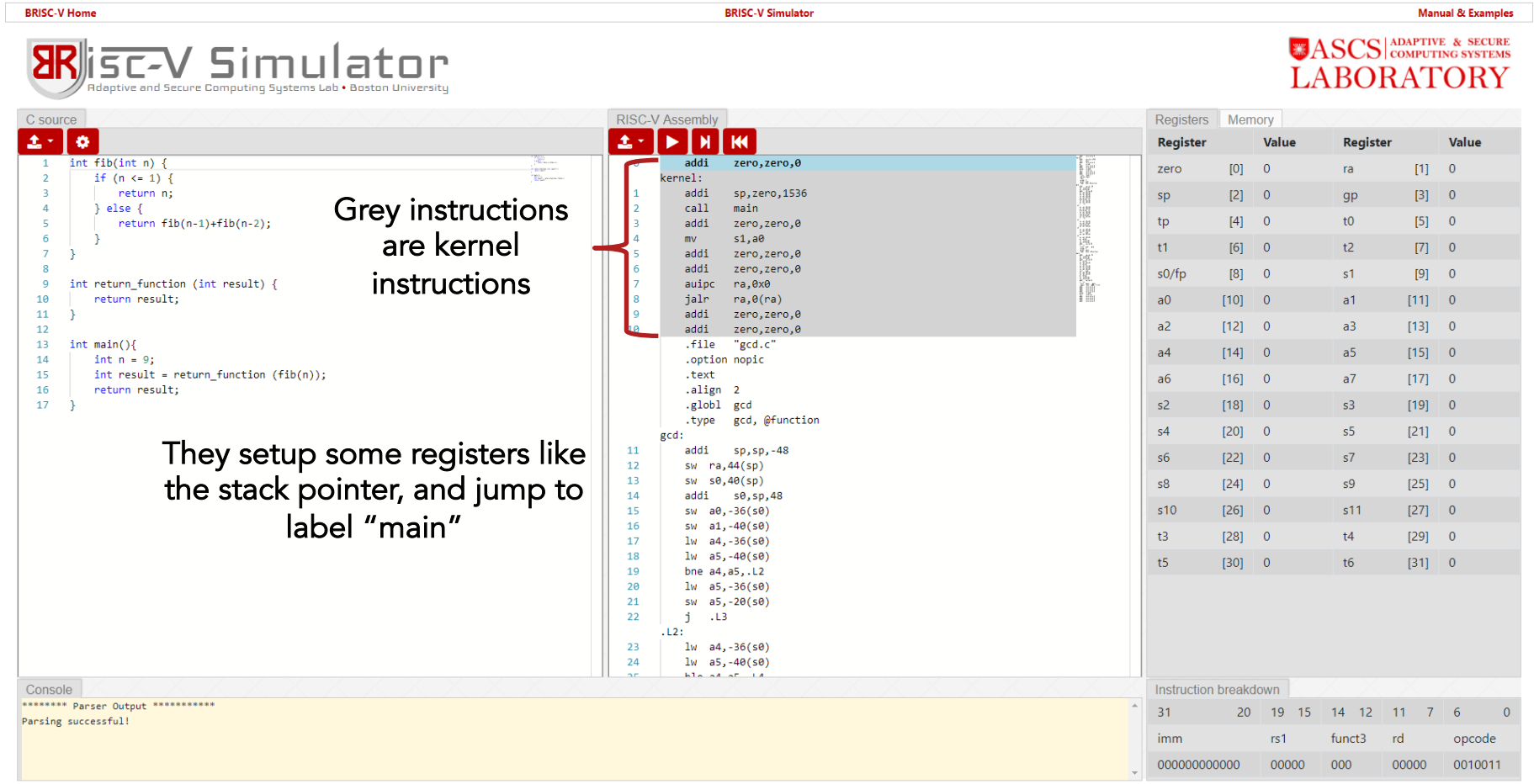

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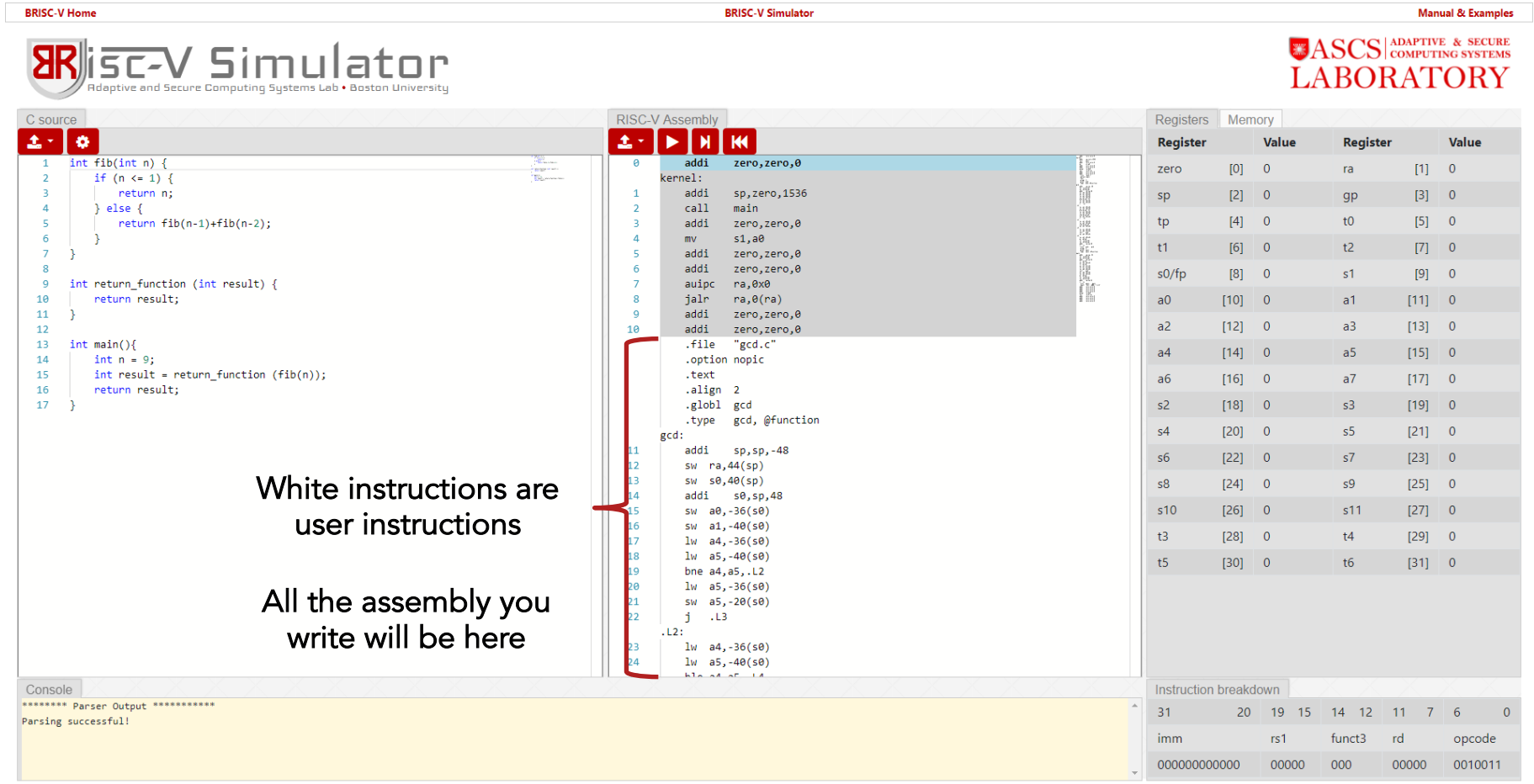
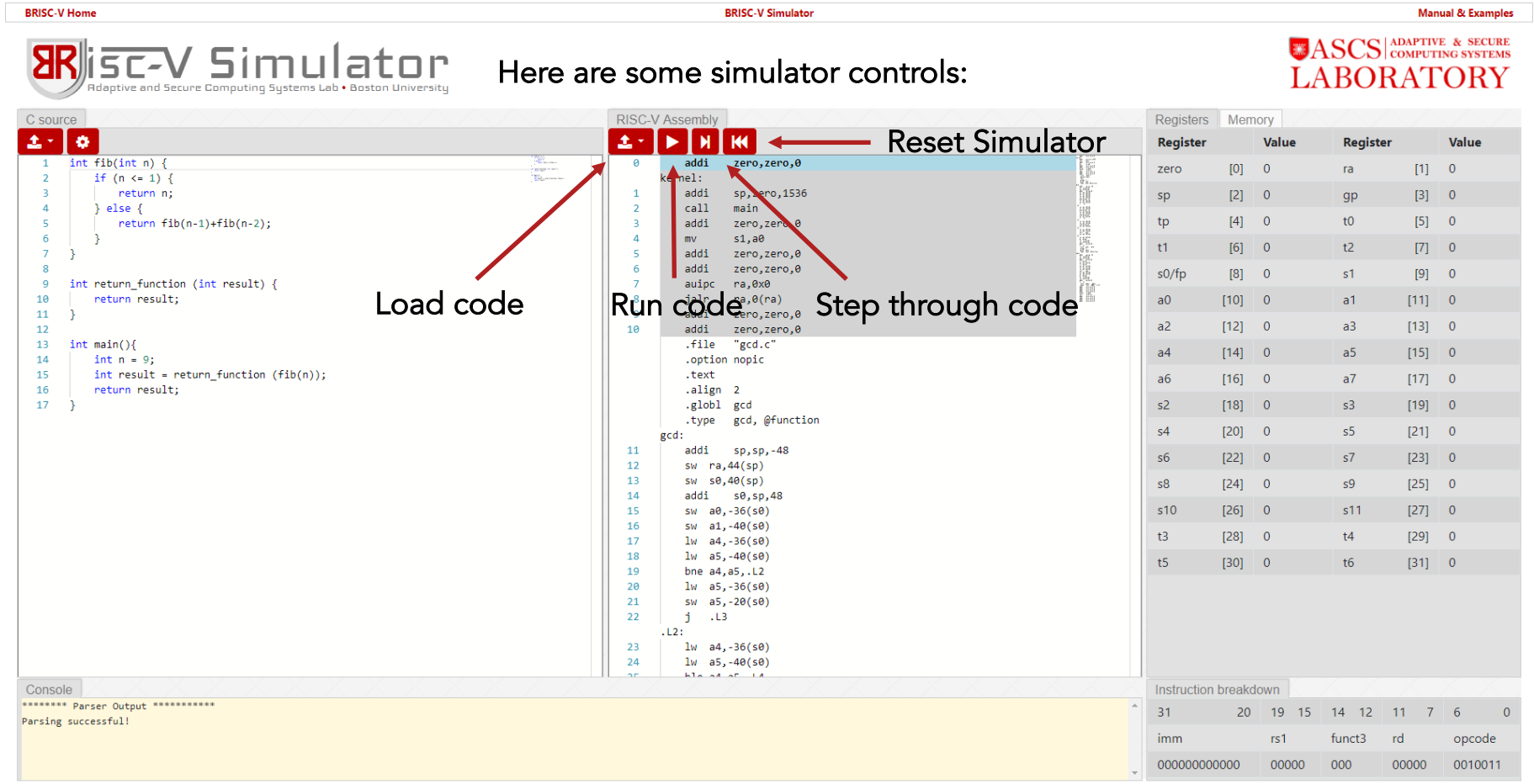
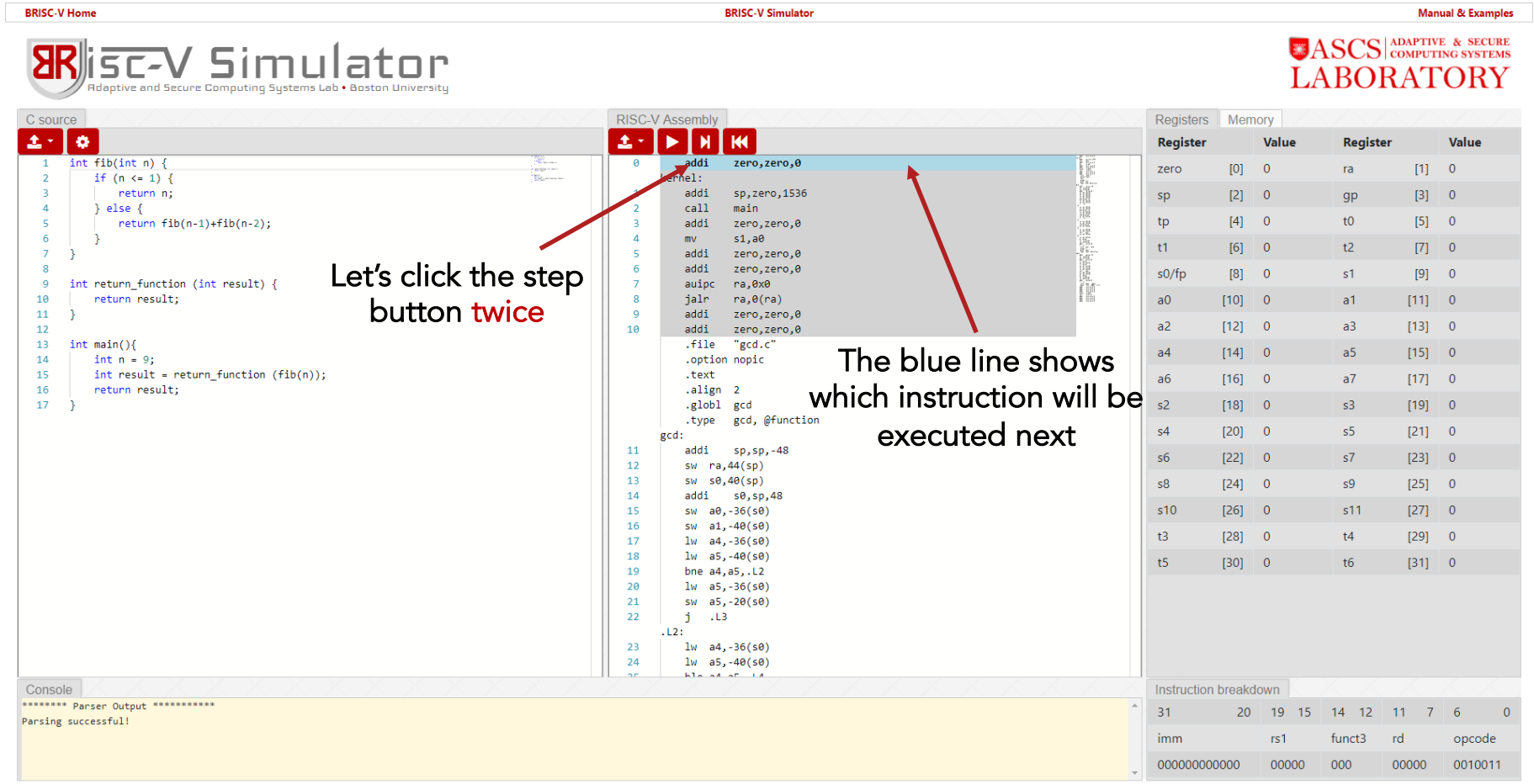


Simulator Controls



Stepping Through a Program



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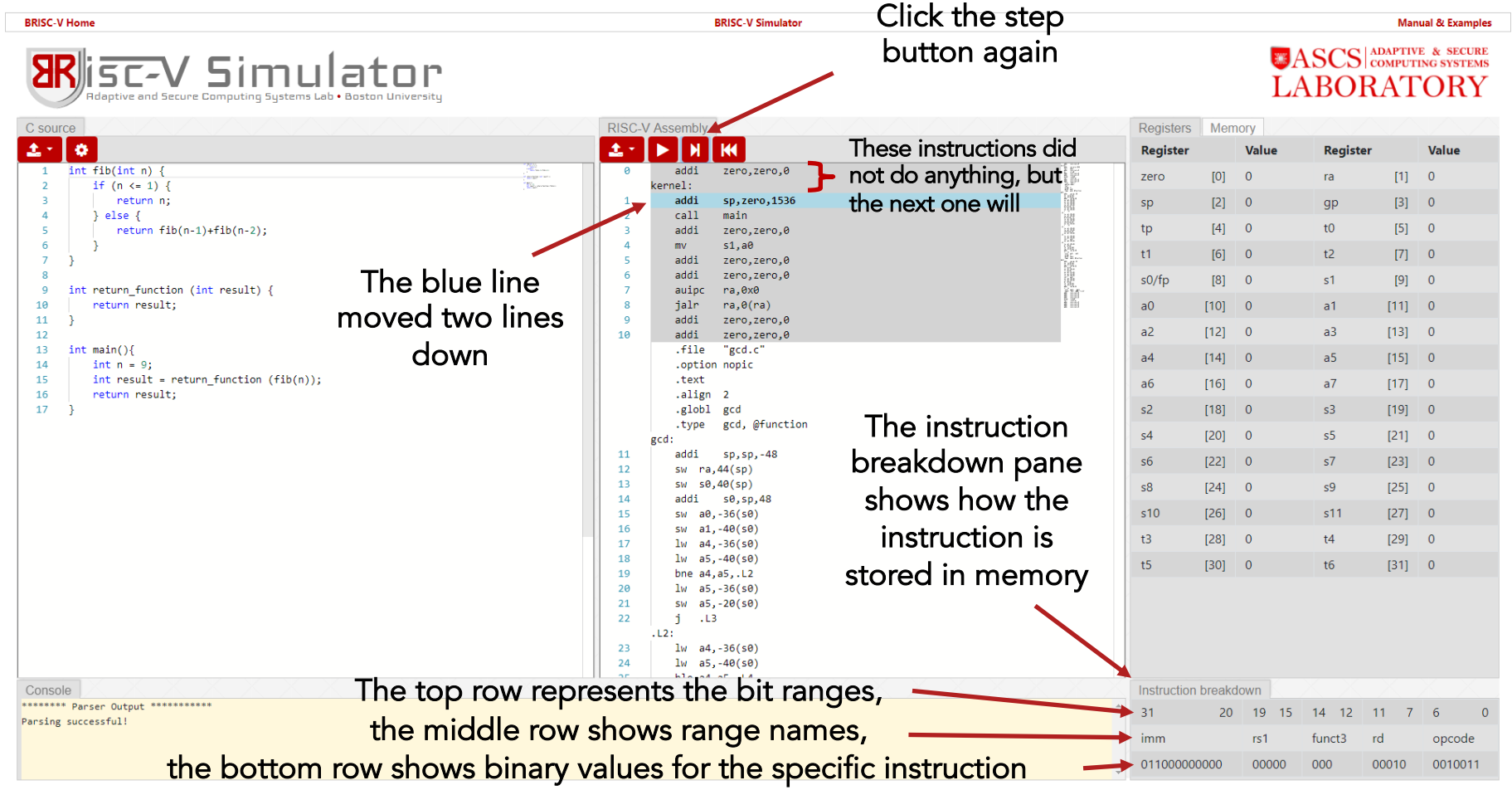
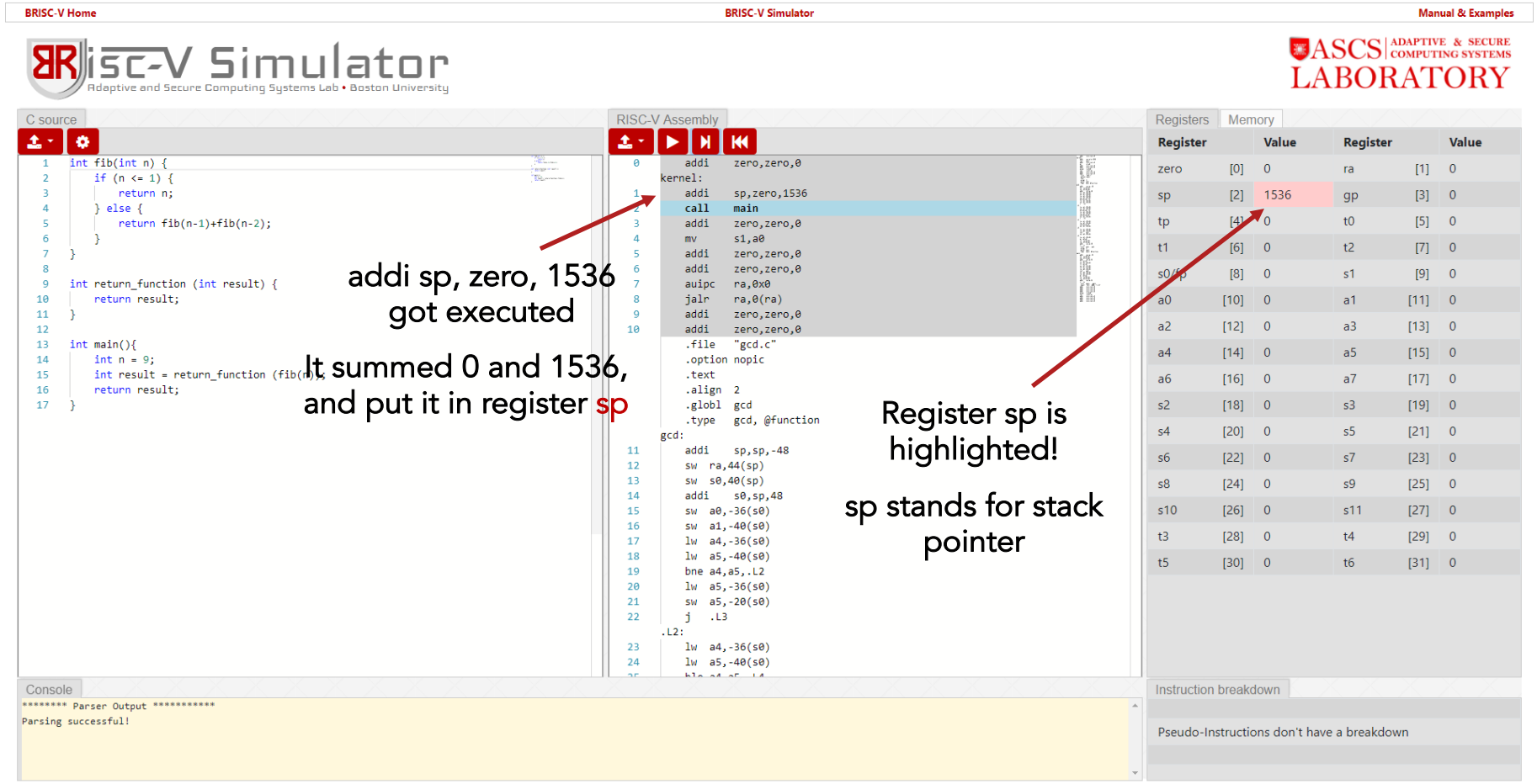
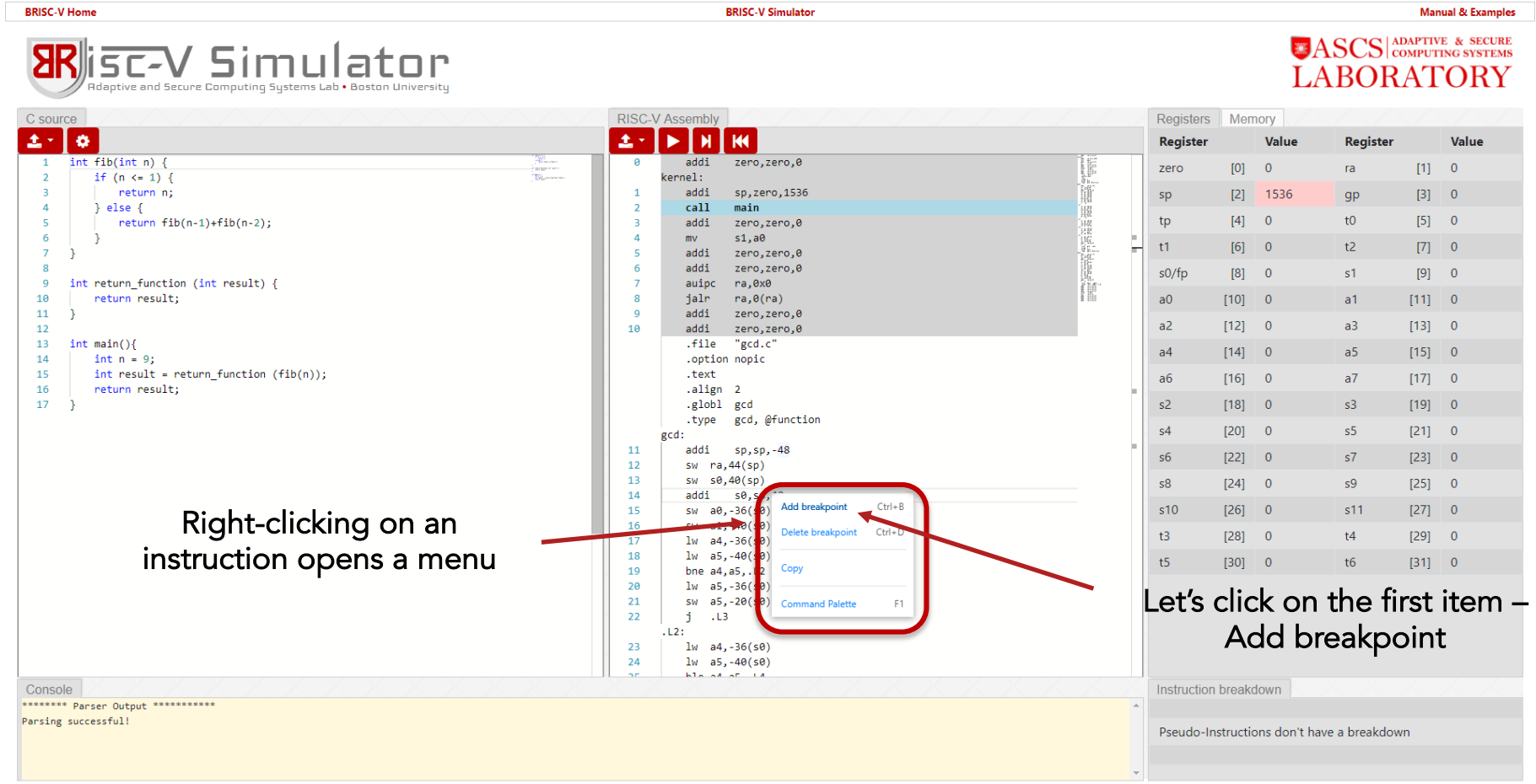


addi Changed the Register File



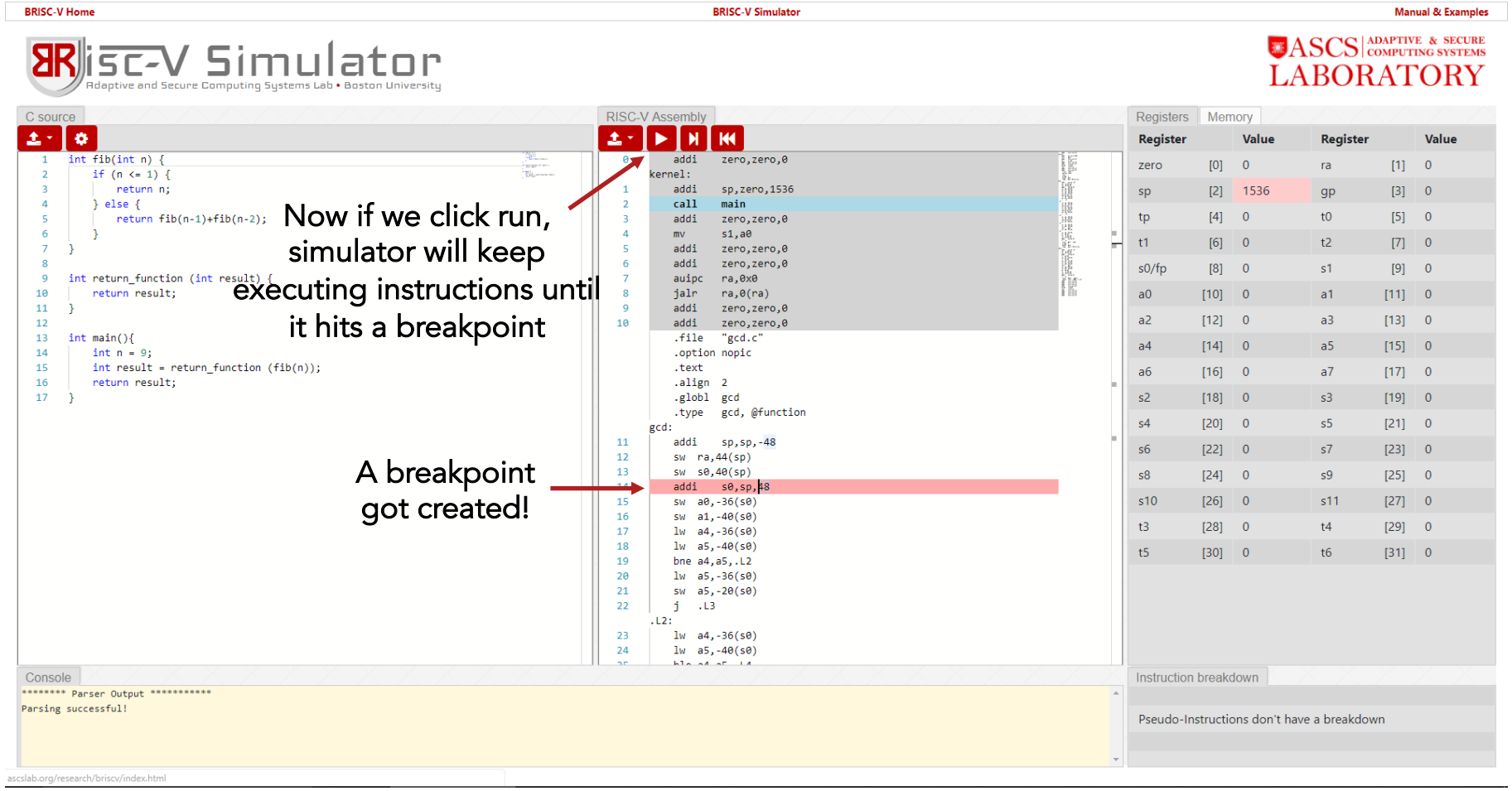
Setting Breakpoints



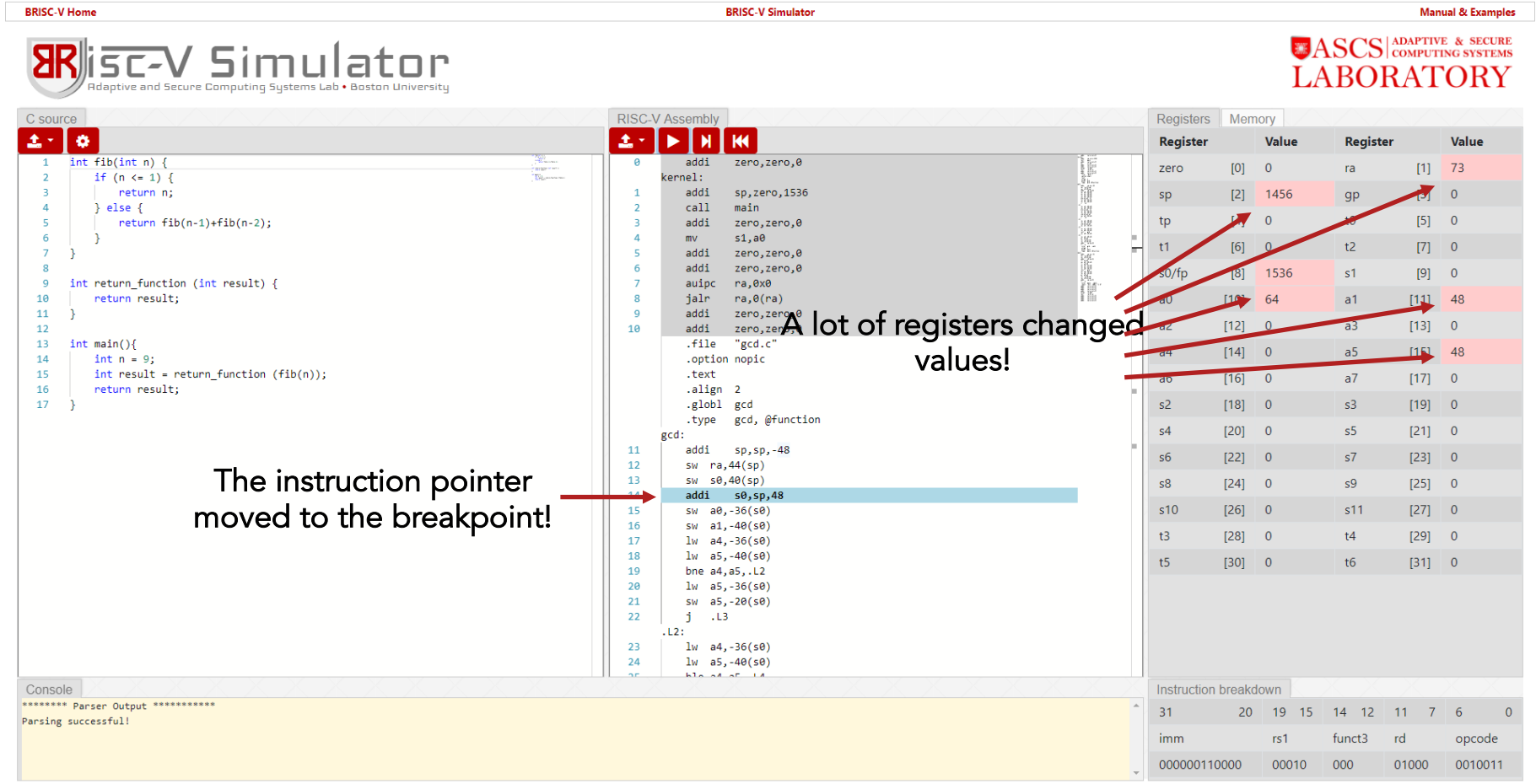
Right-clicking on an instruction opens a menu

Let's click on the first item – Add breakpoint

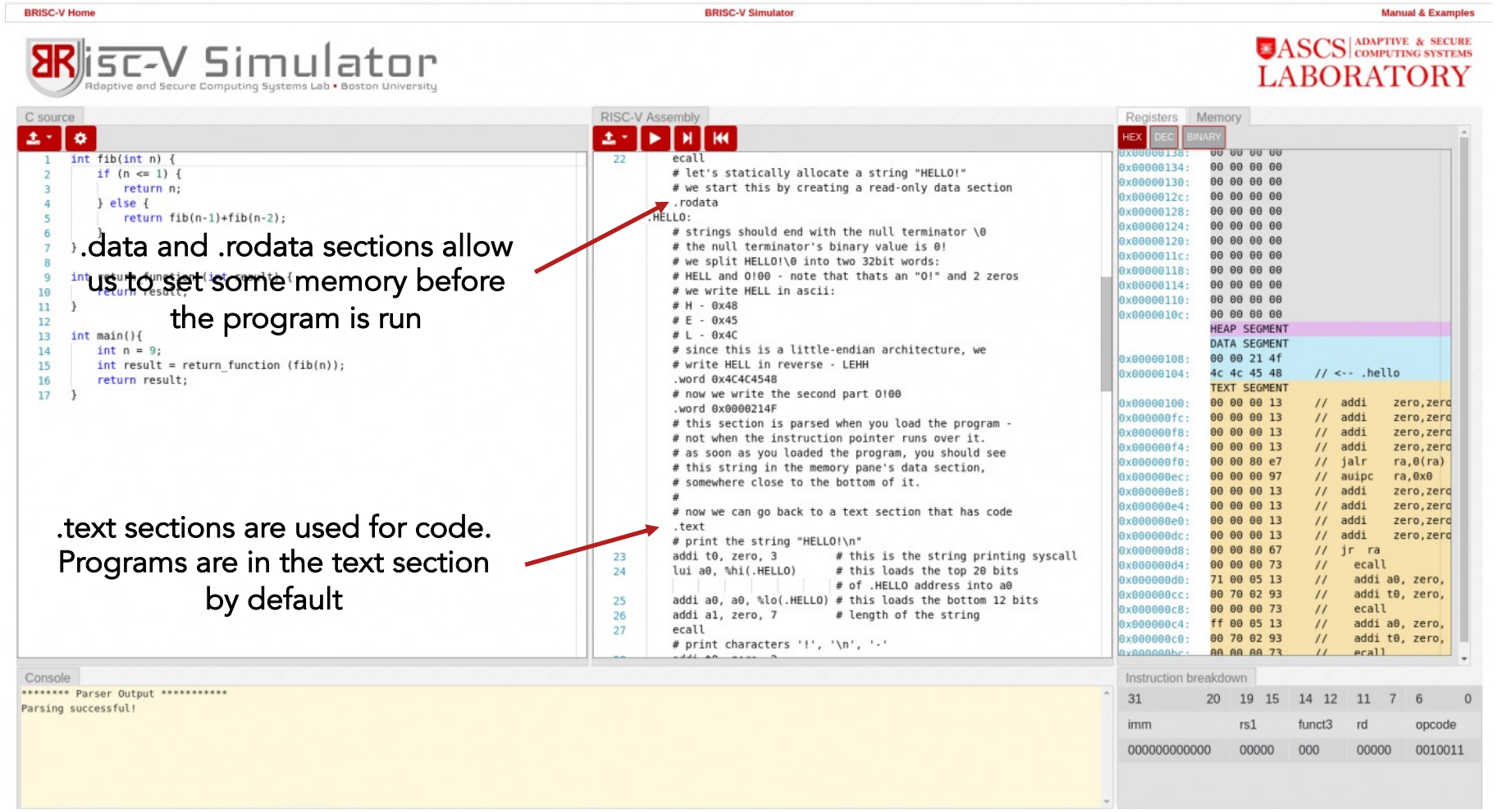
Setting Breakpoints



Running Code until a Breakpoint



Text and Data Sections



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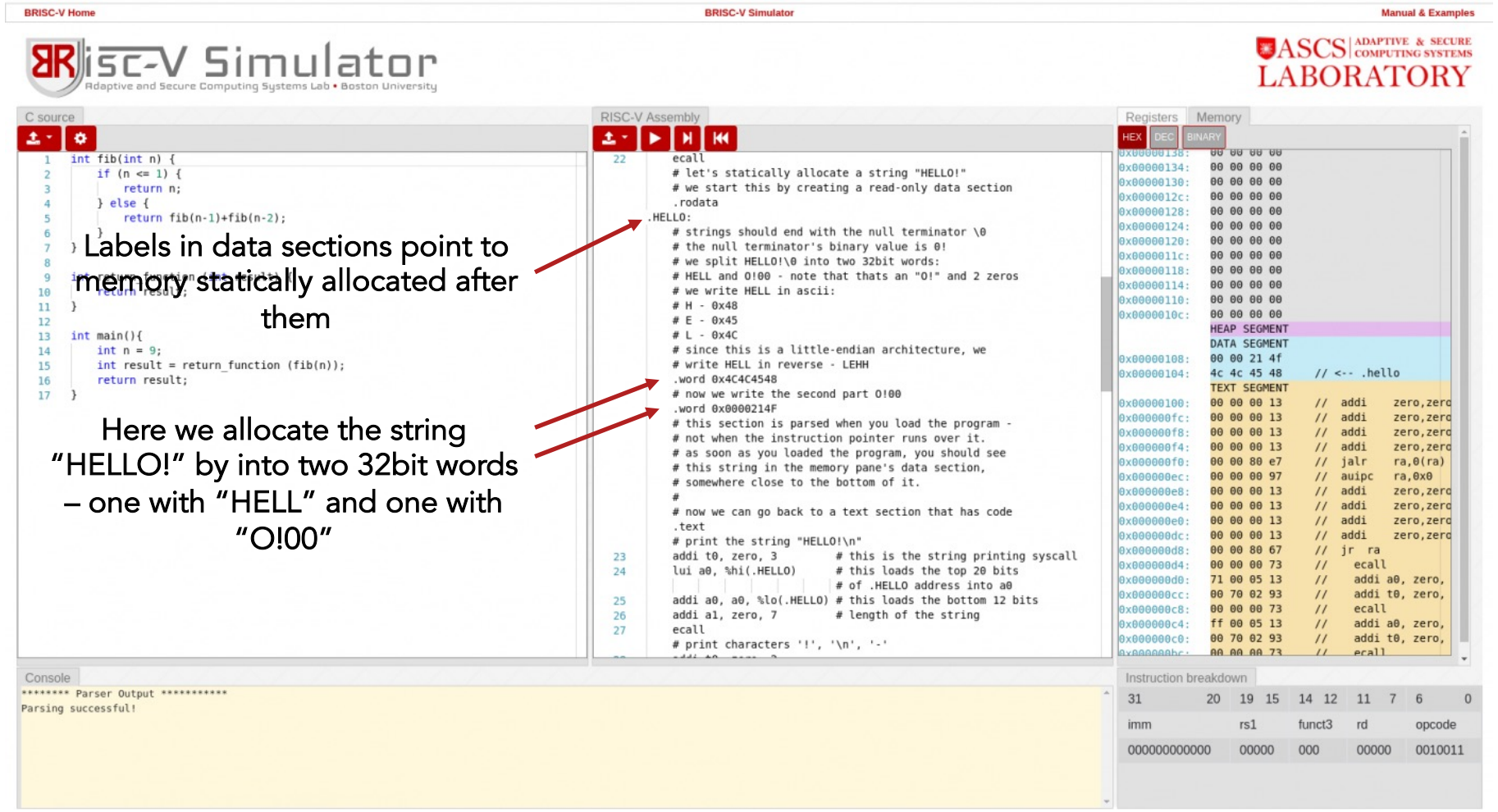

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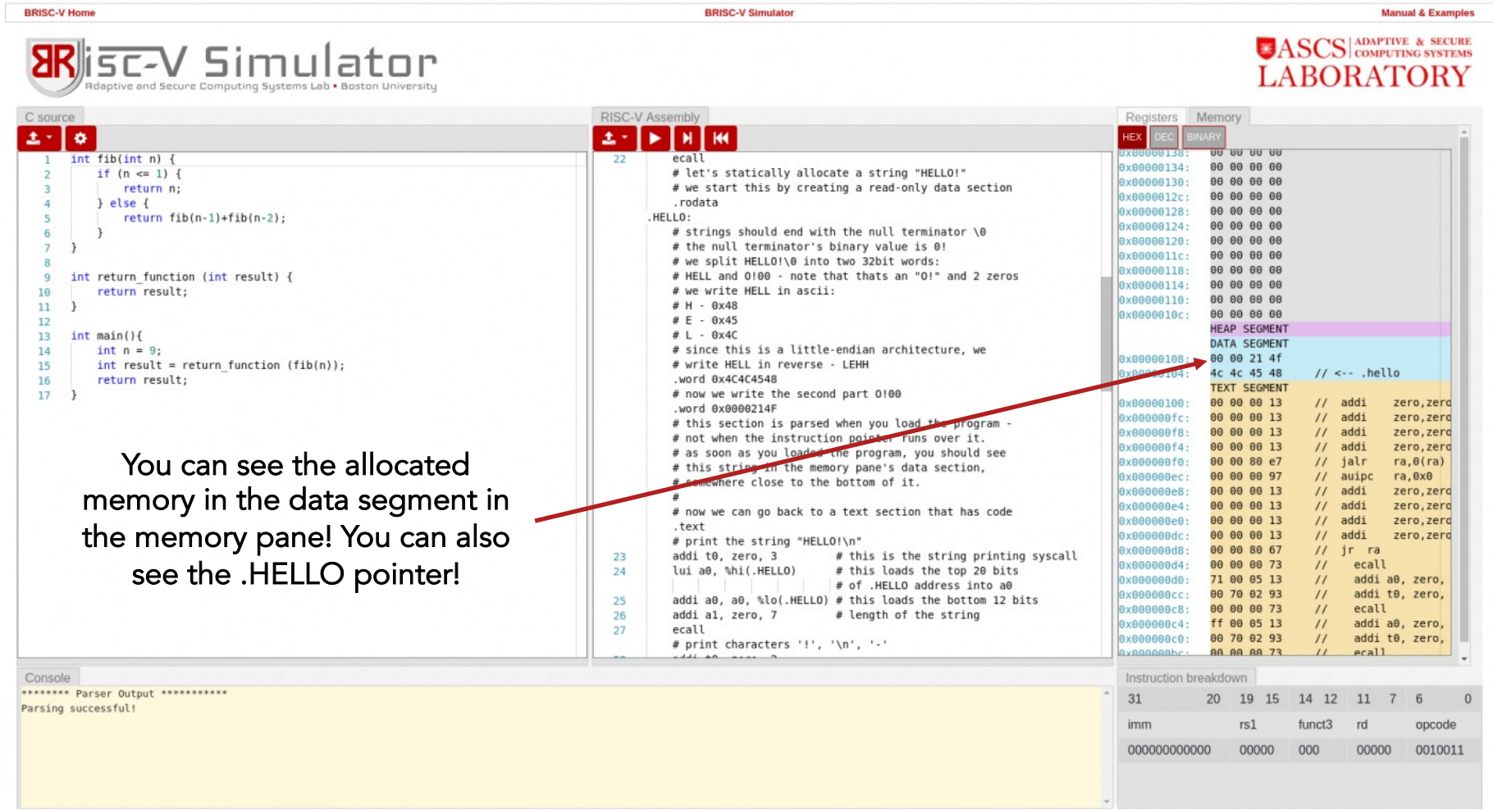
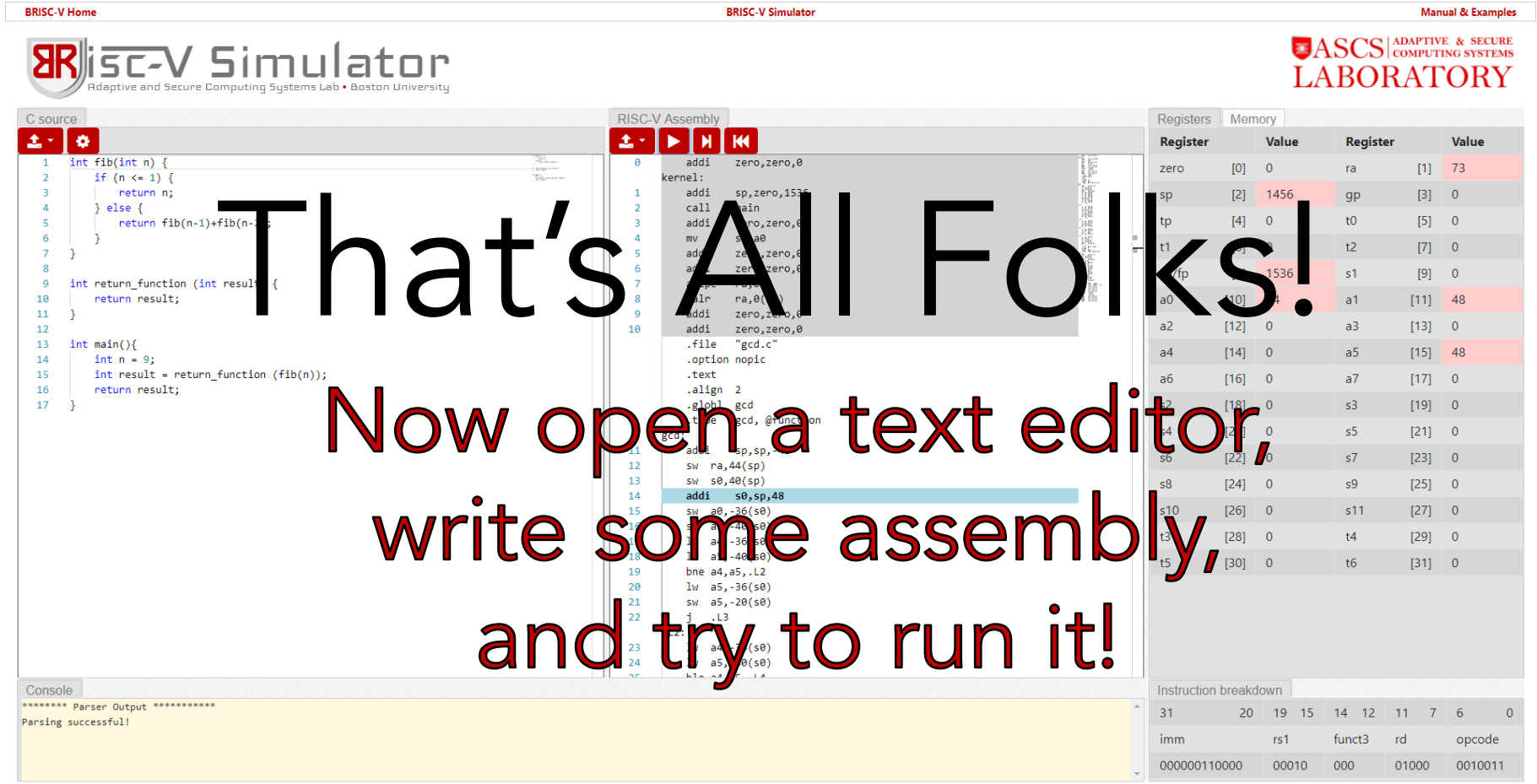


System Calls

- We also provide some simple system calls
- System calls are used for functionalities provided by the operating system
  - Think file systems, IO, etc.
- In RISC-V, system calls look something like:
  - Put the type of system call you want in register t0
    - More about that on the next slide
  - Put any arguments you may have in a0 and a1
  - Call instruction ECALL
  - If the system call has return values, they will be in a0
- To really get familiar with syscalls, try running the example syscall file in the simulator

Supported Syscalls

Syscall	Syscall ID (put this in t0)	Description
Print integer	1	Print integer value in a0 to console
Print char	2	Print ascii value in a0 to console
Print string	3	Print string with address in a0 and length in a1 to console
Read integer	4	Read integer from console into a0
Read char	5	Read character from console into a0 as an ascii value
Read string	6	Read string of length given in a1 from console and store it at address in a0
SBRK	7	Dynamically allocate the amount of bytes specified in a0. The pointer to the beginning of the newly allocated memory will be stored in a0. The value in a0 can be negative, if you want to deallocate some memory!


BRISC-V Home
BRISC-V Simulator
Manual & Examples
BRISC-V Simulator
Adaptive and Secure Computing Systems Lab • Boston University
ASCS ADAPTIVE & SECURE COMPUTING SYSTEMS LABORATORY
C source
RISC-V Assembly
Registers
Memory
Console
Instruction breakdown
That's All Folks!
Now open a text editor,
write some assembly,
and try to run it!